

1. Describe how you might use memory protection hardware to implement an approximate LRU algorithm (e.g., the clock algorithm).
2. Consider the following page-reference string:

1, 2, 3, 4, 2, 1, 5, 6, 2, 1, 2, 3, 7, 6, 3, 2, 1, 2, 3, 6

Fill in the following table with the number of page faults for the given physical memory sizes and replacement algorithms.

<b>Number of Frames</b>	<b>OPT</b>	<b>FIFO</b>	<b>LRU</b>	<b>LFU</b>
3				
4				
5				

Assume that initially there are no resident pages.